

FTS2004

Ultrahigh-Speed Switching Applications

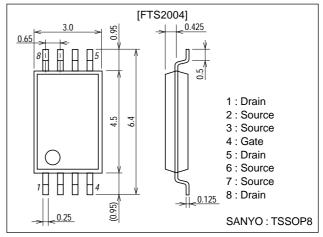
Features

- · Low ON resistance.
- · 4V drive.
- · Mounting height 1.1mm.

Package Dimensions

unit:mm

2147A



Specifications

Absolute Maximum Ratings at Ta = 25°C

| Parameter | Symbol | Conditions | Ratings | Unit |
|-----------------------------|------------------|---|-------------|------|
| Drain-to-Source Voltage | V _{DSS} | | 30 | V |
| Gate-to-Source Voltage | V _{GSS} | | ±20 | V |
| Drain Current (DC) | I _D | | 4 | Α |
| Drain Current (pulse) | I _{DP} | PW≤10µs, duty cycle≤1% | 25 | А |
| Allowable Power Dissipation | PD | Mounted on a ceramic board (1000mm ² ×0.8mm) | 1.3 | W |
| Channel Temperature | Tch | | 150 | °C |
| Storage Temperature | Tstg | | -55 to +150 | °C |

Electrical Characteristics at Ta = 25°C

| Parameter | Symbol | Conditions | Ratings | | | Unit |
|--|-----------------------|---|---------|-----|-----|------|
| | | | min | typ | max | Oill |
| Drain-to-Source Breakdown Voltage | V _(BR) DSS | I _D =1mA, V _{GS} =0 | 30 | | | V |
| Zero-Gate Voltage Drain Current | IDSS | V _{DS} =30V, V _{GS} =0 | | | 10 | μΑ |
| Gate-to-Source Leakage Current | IGSS | V _{GS} =±16V, V _{DS} =0 | | | ±10 | μΑ |
| Cutoff Voltage | V _{GS(off)} | $V_{DS}=10V$, $I_{D}=1mA$ | 1.0 | | 2.4 | V |
| Forward Transfer Admittance | yfs | V _{DS} =10V, I _D =4A | 5 | 8 | | S |
| Static Drain-to-Source On-State Resistance | R _{DS(on)} 1 | I _D =4A, V _{GS} =10V | | 36 | 46 | mΩ |
| | R _{DS(on)} 2 | I _D =4A, V _{GS} =4V | | 58 | 78 | mΩ |
| Input Capacitance | Ciss | V _{DS} =10V, f=1MHz | | 460 | | pF |
| Output Capacitance | Coss | V _{DS} =10V, f=1MHz | | 250 | | pF |
| Reverse Transfer Capacitance | Crss | V _{DS} =10V, f=1MHz | | 120 | | pF |

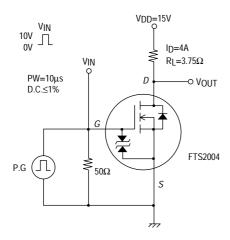
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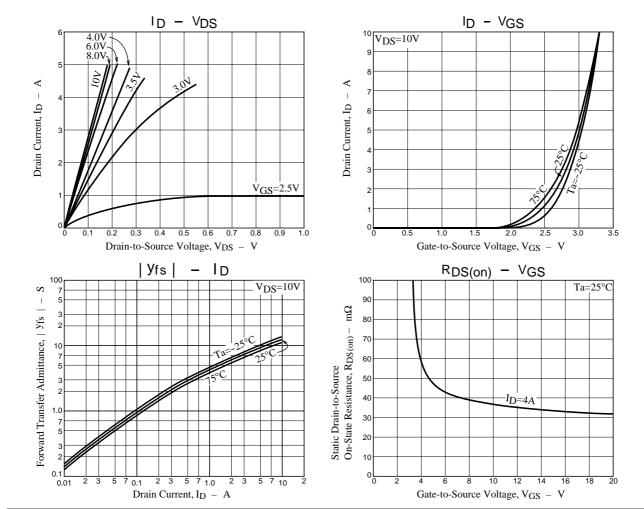
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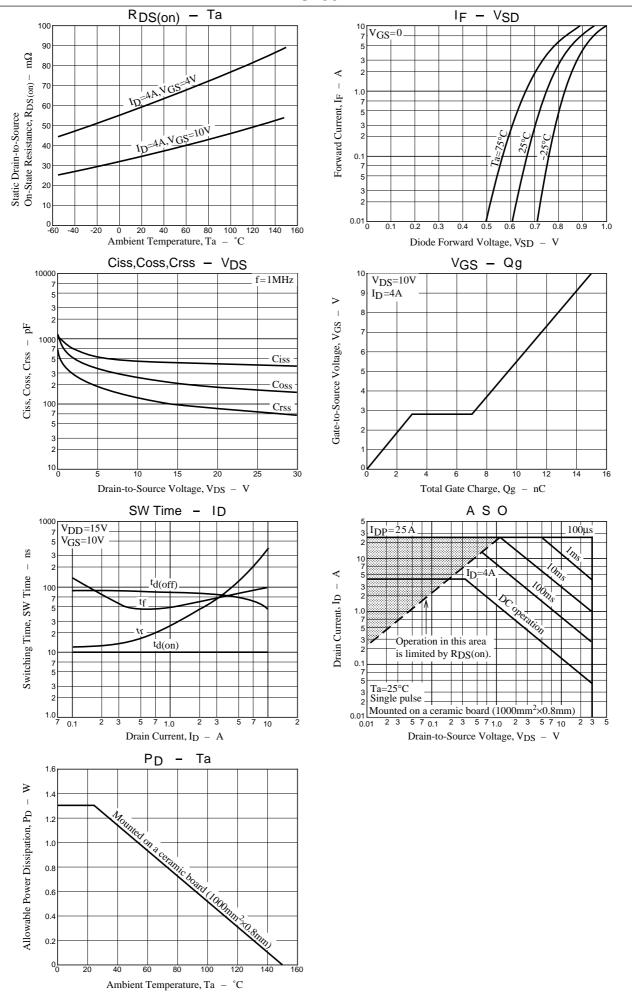
| Parameter | Symbol | Conditions | Ratings | | | Unit |
|-------------------------------|--------------------|--|---------|------|-----|--------|
| | | | min | typ | max | O IIII |
| Turn-ON Delay Time | t _{d(on)} | See specified Test Circuit | | 10 | | ns |
| Rise Time | t _r | See specified Test Circuit | | 90 | | ns |
| Turn-OFF Delay Time | td(off) | See specified Test Circuit | | 70 | | ns |
| Fall Time | t _f | See specified Test Circuit | | 75 | | ns |
| Total Gate Charge | Qg | V _{DS} =10V, V _{GS} =10V, I _D =4A | | 15 | | nC |
| Gate-to-Source Charge | Qgs | V_{DS} =10V, V_{GS} =10V, I_{D} =4A | | 3 | | nC |
| Gate-to-Drain "Miller" Charge | Qgd | V _{DS} =10V, V _{GS} =10V, I _D =4A | | 4 | | nC |
| Diode Forward Voltage | V _{SD} | I _S =4A, V _{GS} =0 | | 0.85 | 1.2 | V |

Switching Time Test Circuit





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